Statement of Volatility – Dell PowerEdge T30

Dell PowerEdge T30 contains both volatile and non-volatile (NV) components. Volatile components lose their data immediately upon removal of power from the component. Non-volatile components continue to retain their data even after the power has been removed from the component. Components chosen as user-definable configuration options (those not soldered to the motherboard) are not included in the Statement of Volatility. Configuration option information (pertinent to options such as microprocessors, remote access controllers, and storage controllers) is available by component separately. The following NV components are present in the PowerEdge T30 server.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size
Planer				
PCH Internal CMOS RAM	Non-Volatile	1	U4	256 Bytes
BIOS SPI Flash	Non-Volatile	1	SPI1	16 MB
SIO	Non-Volatile	1	U138	2K Bytes
TPM	Non-Volatile	1	UF1	24K Bytes
Embedded NV memeory in PCIe to PCI bridge	Non-Volatile	1	UK1	4096 Bytes
SIO	Volatile	1	U138	256 Bytes
System Memory	Volatile	Up to 4	DIMM1~DIMM4	Up to 16GB per DIMM
Power Supplies				
PSU FW	Non-Volatile	1 per PSU	Varies by part number	Up to 2MB. Varies by part number

Item	Type (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	Purpose? (e.g. boot code)
Planer			
PCH Internal CMOS RAM	Battery-backed CMOS RAM	No	Real-time clock and BIOS configuration settings

Item	Type (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	Purpose? (e.g. boot code)
BIOS SPI Flash	SPI Flash	Yes	Boot code, system configuration information, UEFI environment, Flash Disceptor, ME
SIO	Embedded NV memory	No	Management system's fan speed/ thermal.
TPM	Embedded NV memory	No	Encode information for security
Embedded NV memeory in PCIe to PCI bridge	Embedded NV memory	No	Transfer PCIE signals to PCI
SIO	RAM	No	Not utilized
System Memory	RAM	Yes	System OS RAM
Power Supplies			
PSU FW	Embedded microcontroller flash	No	Power Supply operation, power management data and fault behaviors

Item	How is data input to this memory?	How is this memory write protected?
Planer		
PCH Internal CMOS RAM	BIOS	N/A – BIOS only control
BIOS SPI Flash	SPI interface via PCH	Software write protected
SIO	SPI interface via iDRAC	Embedded iDRAC subsystem firmware actively controls sub area based write protection as needed.
Embedded NV memory in PCIe to PCI bridge	Not utilized	Not accessible
TPM	TPM	N/A
SIO	Not utilized	Not accessible
System Memory	System OS RAM	System OS
System Memory	System OS	OS Control
Power Supplies		

Item	How is data input to this memory?	How is this memory write protected?
PSU FW	Different vendors have different utilities and tools to load the data to memory.	Protected by the embedded microcontroller. Special keys are used by special vendor provided utilities to unlock the ROM with various CRC checks during load.

NOTE: For any information that you may need, direct your questions to your Dell Marketing contact.

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